2. (Original) A disk device according to claim 1, wherein:

a high-reliability disk to which both a low-level error-correction code and a high-level error-correction code are written and a disk to which only the low-level error-correction code is written are loadable into said disk drive;

when the high-reliability disk is loaded, the processing circuit of said disk drive performs low-level error correction, and then said host computer, to which the correction data is supplied, performs high level error correction; and

when the latter disk is loaded, the processing circuit of said disk drive performs low-level error correction, and said host computer processes the corrected data.

3. (Original) A disk device according to claim 1, wherein:

information is written to the disk for discriminating a high-reliability disk to which both a low-level error-correction code and a high-level error-correction code are written from a disk to which only the low-level error correction is written ; and

said host computer determines which disk is inserted based on the information.

4. (Currently Amended) A storage device comprising:

a low-level error correction unit within a drive configured to detect, and when necessary, correct errors ~~on the fly~~ in data written to a single sector comprising 512 bytes of a storage area of a disk;

a read mechanism coupled to the low-level error correction unit; and

a host coupled to the drive comprising a high-level error correction code unit configured to detect, and when necessary, correct errors ~~on the fly~~ in data stored in more than one sector of the storage area of the disk.

5. (Previously Added) The storage device of claim 4 wherein the low-level error correction unit detects and, when necessary, corrects an error in the data storage area that corresponds to a physical address.

6. (Previously Added) The storage device of claim 5, wherein the high-level error correction code unit detects and, when necessary, corrects an error in the data storage area that corresponds to a plurality of physical addresses.
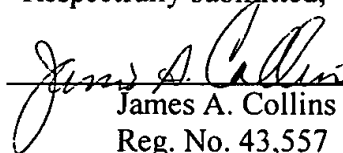
7. (Previously Added) The storage device of claim 4, wherein the high-level error correction code unit detects and, when necessary, corrects an error in the data storage area that corresponds to a plurality of physical addresses.

8. (Previously Added) The storage device of claim 4 wherein both the drive and the host are configured to detect and, when necessary, correct errors in data in a common sector.

9. (Currently Amended) The storage device of claim 4 wherein the read mechanism comprises a read/write mechanism.

Respectfully submitted,

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